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## APPLICANTS

Akira Ishikawa, Kanagawa, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/951,384 09/14/2001 PAT 6,682,963

*VERIFIED*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-280864 09/14/2000

*VERIFIED*

JAPAN 2000-280902 09/14/2000

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 09/13/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	JAPAN	25	25	5

## ADDRESS

26171  
 FISH & RICHARDSON P.C.  
 P.O. BOX 1022  
 MINNEAPOLIS , MN  
 55440-1022

## TITLE

Semiconductor device and manufacturing method thereof

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